

## FLASH ADC

## 32-Channel Analog-to-Digital Converter

35 MHz

**SMA** 

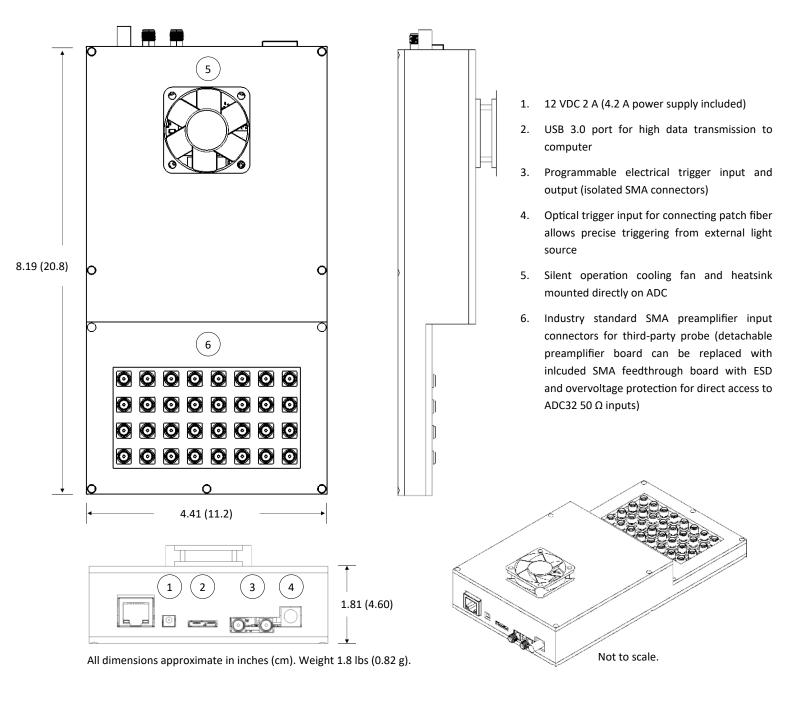


- Compact, external USB housing for easy instrument integration
- Industry standard SMA input connectors
- Continuous analog-to-digital conversion with no buffering or multiplexing allowing faster data transmission and trigger rates
- Integrated amplifier chips with digitally controlled gain
- Generate trigger output at defined rate or repetition of external trigger input with programmed delay.
- Sync external hardware with data acquisition using electronic or optical IN and OUT ports located on the unit housing
- Includes standalone control software based on the MATLAB® computing environment and backend SDK written in C++ compatible with many frontend languages such as LabView, MATLAB® and Python<sup>TM</sup>

Channels <sup>(1)</sup>	32
Programmable Gain (2)	44 to 94 dB
Bandwidth @ -6 dB (3)	16 kHz to 35
Sampling Rate	80 MSPS
Resolution	12-bit
Max Trigger / Frame Rate (4)	6000 Hz / fp
Max Points (5)	80,000
Input Impedance (6)	50 kΩ

**Input Connector** 

- All channels fully parallel (simultaneous data acquisition without multiplexing)
- (2) Measured with  $50\Omega$  load (actual gain depends on probe capacitance)
- (3) Low Pass programmable filters available
- (4) 6000Hz sustained with 1000 points 12-bit (limited by USB3 data bandwidth)
- (5) Per frame per channel
- (6) Measured using signal generator and oscilloscope with  $50\Omega$  input



Minimum PC Requirements: 6th generation Genuine Intel® quad-core processor, 8 GB DDR4 RAM. USB3 port on Intel® host controller, 500 GB PCIe 3.0 x4 SSD w/ heatsink, Microsoft Windows 10 64-bit Home

Recommended PC Requirements: 9th generation Genuine Intel® hexa-core processor or better, 16 GB DDR4 RAM, USB3 port on Intel® host controller, 1 TB PCle 3.0 x4 SSD w/ heatsink (e.g. Samsung 970 Pro), Microsoft Windows 10 64-bit Pro

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**FLASH**<sup>™</sup> DAQ32 is classified EAR99 and does not require an export license.

